

FIG. 1

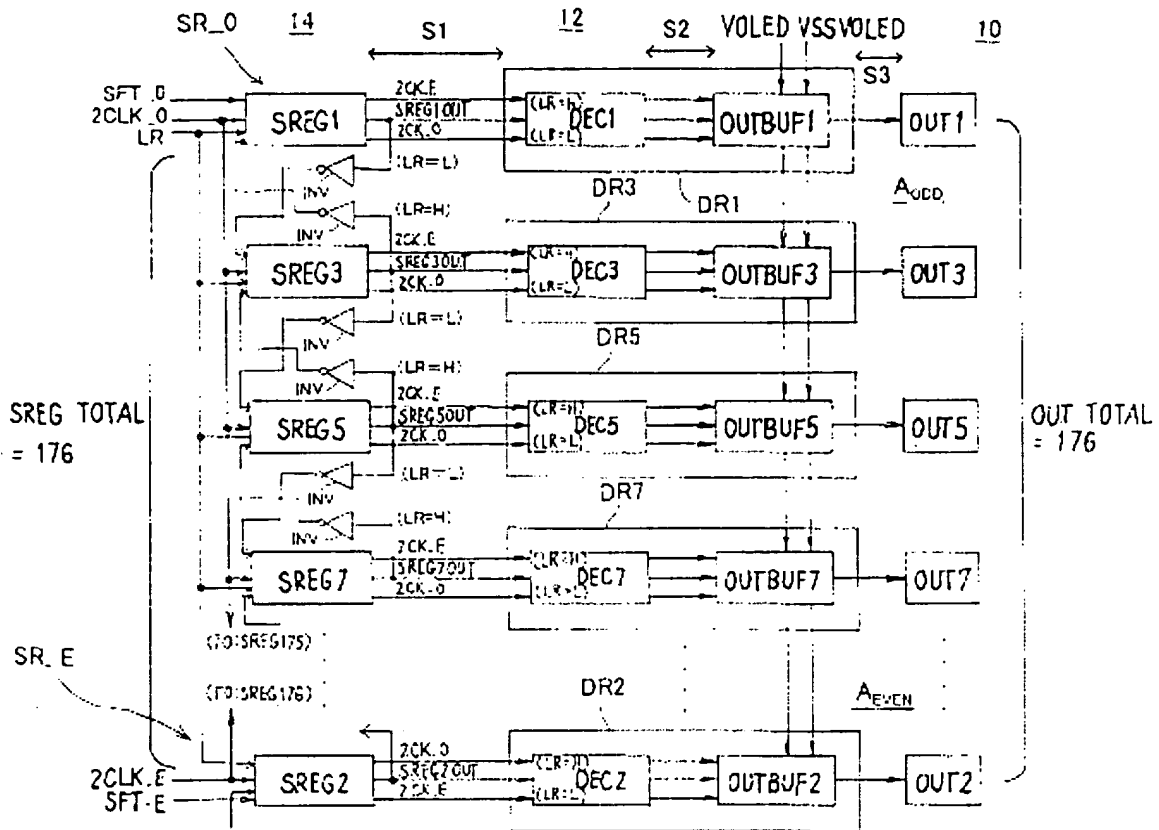


FIG. 2

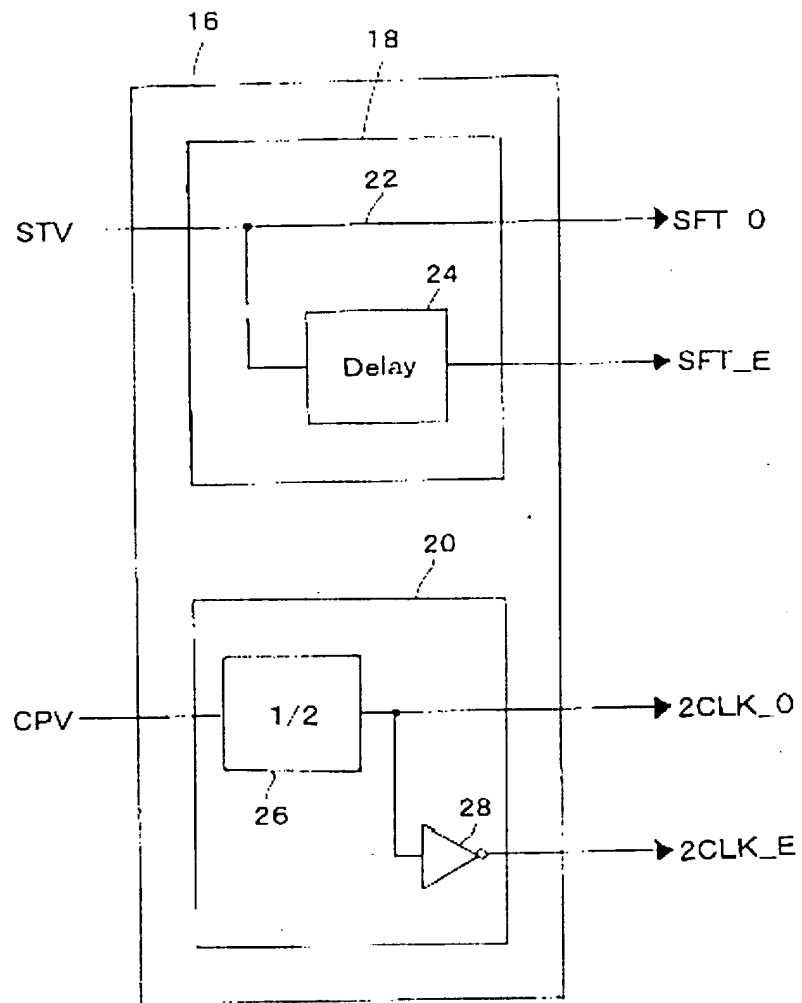


FIG. 3

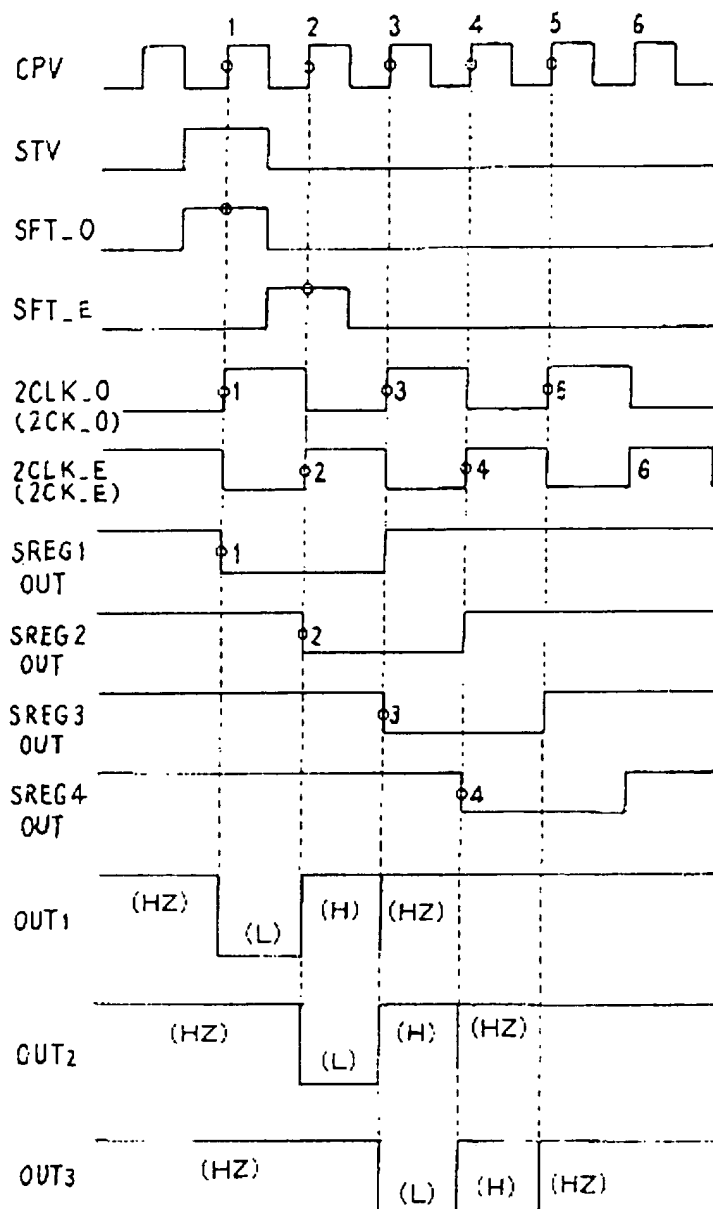


FIG. 4

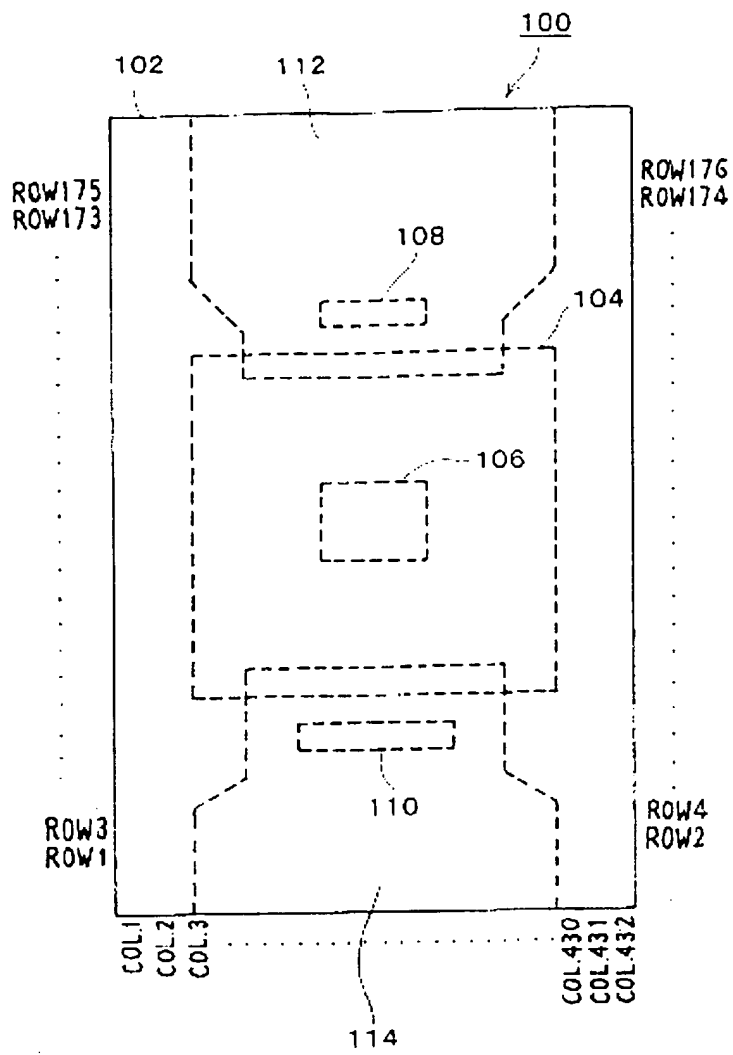


FIG. 5

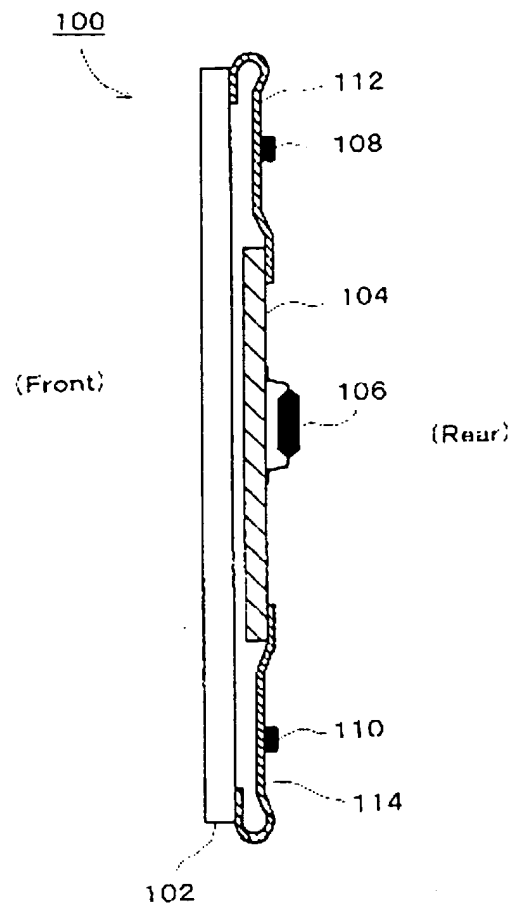


FIG. 6

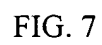


FIG. 7

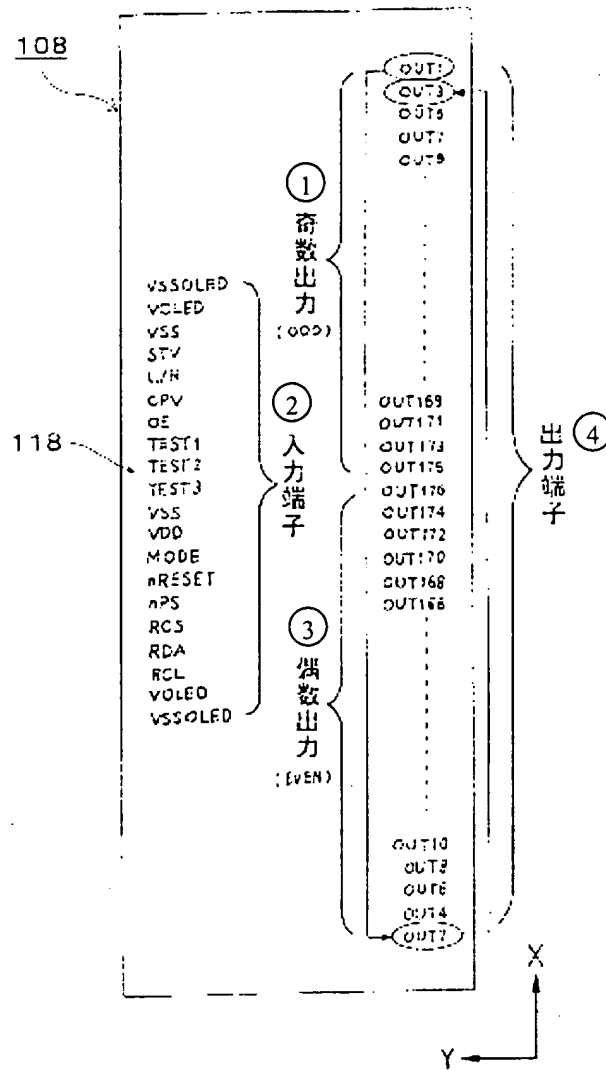


FIG. 8

- Key:
- 1 Odd-numbered output
  - 2 Input terminals
  - 3 Even-numbered output
  - 4 Output terminals



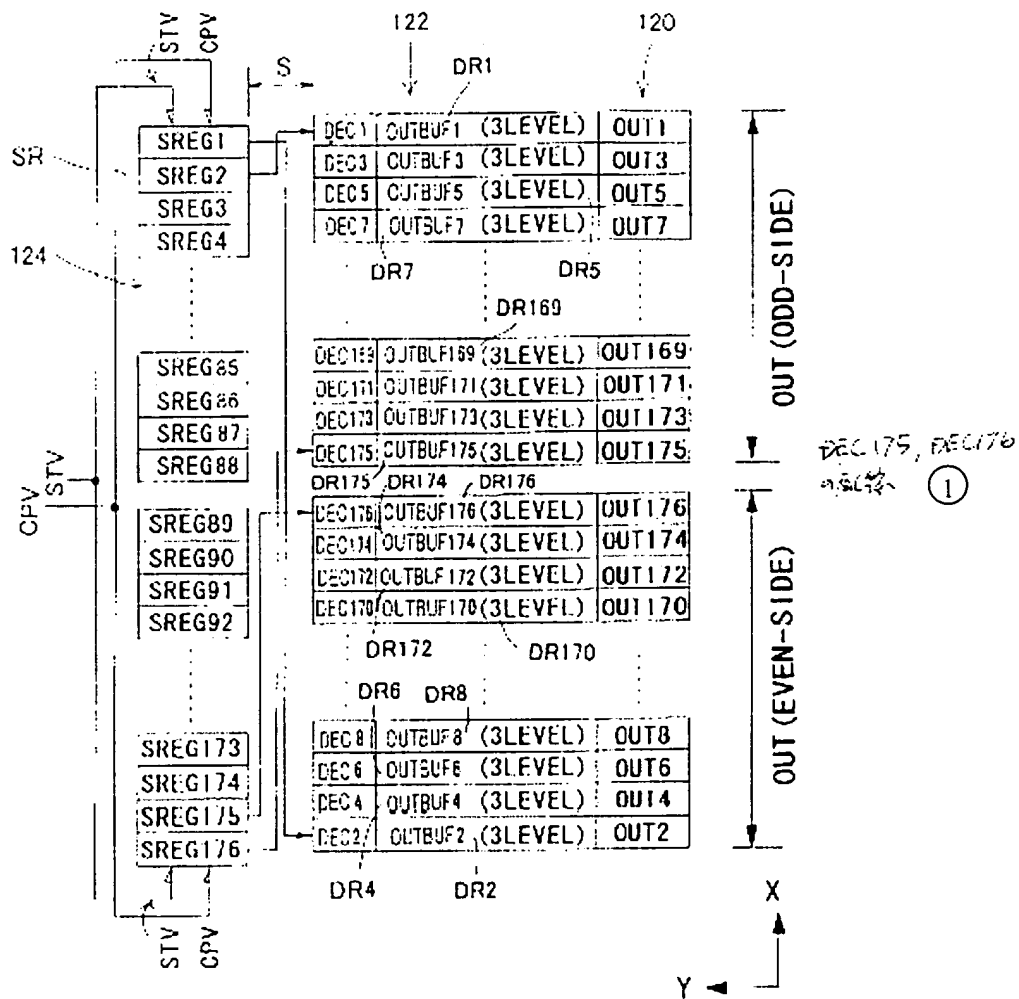


FIG. 9

Key: 1 Wiring of DEC175, DEC176

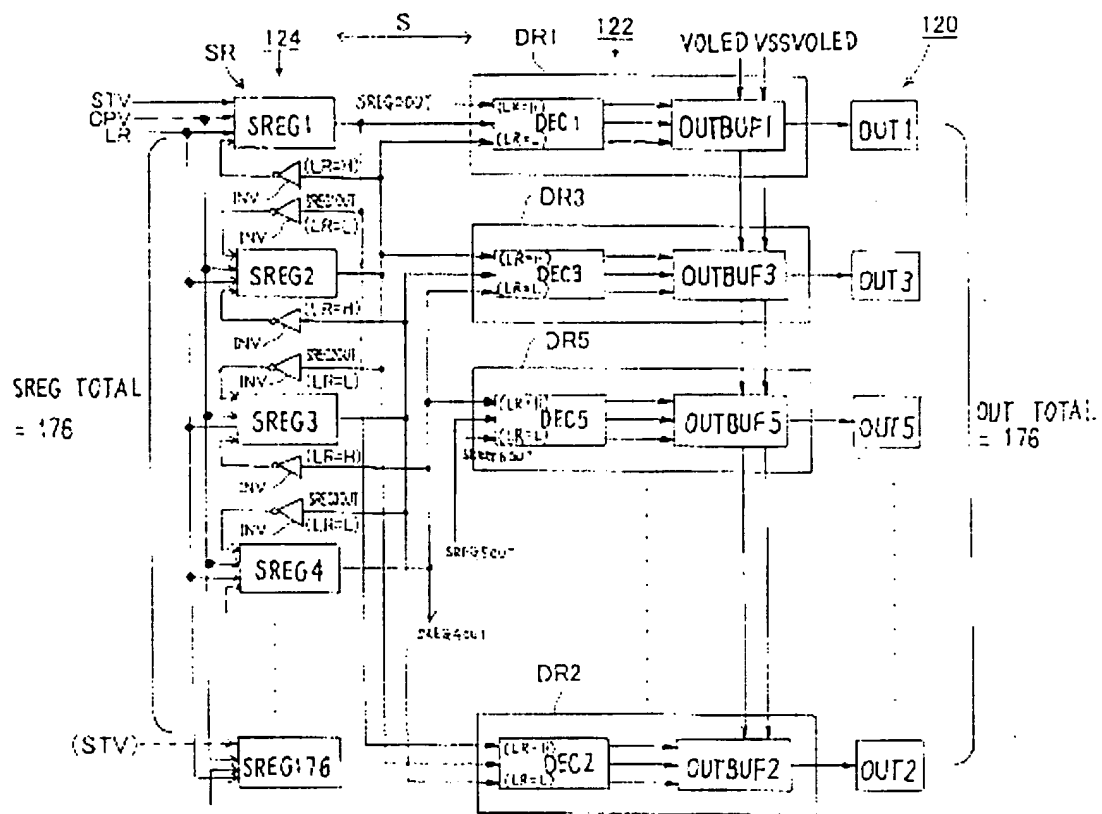


FIG. 10